

Notice of Allowability

Application No.

10/696,225

Examiner

Phuong Phu

Applicant(s)

BRUCE, ROBERT

Art Unit

2611

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to the Amendment filed on 5/21/07.
2. ☒ The allowed claim(s) is/are 1-16.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☐ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- | | |
|--|---|
| 1. <input type="checkbox"/> Notice of References Cited (PTO-892) | 5. <input type="checkbox"/> Notice of Informal Patent Application |
| 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 6. <input type="checkbox"/> Interview Summary (PTO-413),
Paper No./Mail Date _____ |
| 3. <input type="checkbox"/> Information Disclosure Statements (PTO/SB/08),
Paper No./Mail Date _____ | 7. <input type="checkbox"/> Examiner's Amendment/Comment |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit
of Biological Material | 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance |
| | 9. <input type="checkbox"/> Other _____ |

DETAILED ACTION

1. This Office Action is responsive to the Amendment filed on 5/21/07. Accordingly, claims 1-16 are currently pending.

REASONS FOR ALLOWANCE

2. Claims 1-16 are allowed.
3. The following is an examiner's statement of reasons for allowance:

-Regarding to independent claim 1, none of prior art of record teaches or suggests a signal edge detector circuit having an output terminal and an input, said signal edge detector circuit comprising: an input transmission line connected to said input and output terminals to provide a path for a signal transition from said input terminal to said output terminal, said input transmission line including a node between said input and output terminals; a first reflection transmission line having first end, said first reflection transmission line connected at a first end to said node providing a reflection of said signal transition, said reflection of a polarity to terminate said signal transition at said output terminal; a second reflection transmission line having a first end, said second reflection transmission line connected at said first end to said node providing a reflection of said signal transition, said reflection of a polarity to cancel out said reflection from said first reflection transmission line at said node.

-Regarding to independent claim 9, none of prior art of record teaches or suggests a clock signal generator comprising a signal edge detector circuit, said signal edge detector circuit comprising an input transmission line connected to said input and output terminals to provide a path for each signal transition in said data stream from said input terminal to said output terminal, said input transmission line including a node between said input and output terminals; a

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first reflection transmission line having a first end, said first reflection transmission line connected at said first end to said node providing a reflection of said signal transition, said reflection of a polarity to terminate said signal transition at said output terminal; and a second reflection transmission line having a first end, said at least one second reflection transmission line connected at said first end to said node providing a reflection of said signal transition, said reflection of a polarity to cancel out said reflection from said first reflection transmission line at said node.

-Regarding to independent claim 16, none of prior art of record teaches or suggests a signal edge detector circuit having an output terminal and an input terminal, said signal edge detector circuit comprising: an input transmission line connected to said input and output terminals to provide a path for a signal pulse from said input terminal to said output terminal, said input transmission line including a node between said input and output terminals; means connected to said node for providing a first reflection of said pulse, said reflection of a polarity to terminate said pulse at said output terminal; means connected to said node for providing a second reflection of said pulse, said reflection of a polarity to cancel out said reflection from said first reflection transmission line at said node.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Phuong Phu whose telephone number is 571-272-3009. The examiner can normally be reached on M-F (8:00 AM - 4:30 PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jay Patel can be reached on 571-272-2988. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Phuong Phu
Phuong Phu
05/24/07

PHUONG PHU
PRIMARY EXAMINER

Phuong Phu
Primary Examiner
Art Unit 2611